

MIMAS

ES BUILD

2010-02-09

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2008/10/01	2008-ECO-016503	B
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC		
DRAWER	Bai, Brain	2008-AGU-25			TITLE MIMAS		
DESIGN	Bai, Brain	2008-AGU-25					
CHECK	Bai, Brain	2008-AGU-25					
RESPONSIBLE							
SIZE = 3					SIZE	CODE	DOC. NUMBER
FILE NAME : XXXX-XXXXXX-XX					A3	CS	6050A2350601
P/N 6050A2215101					REV X01		
					SHEET	1	OF 30

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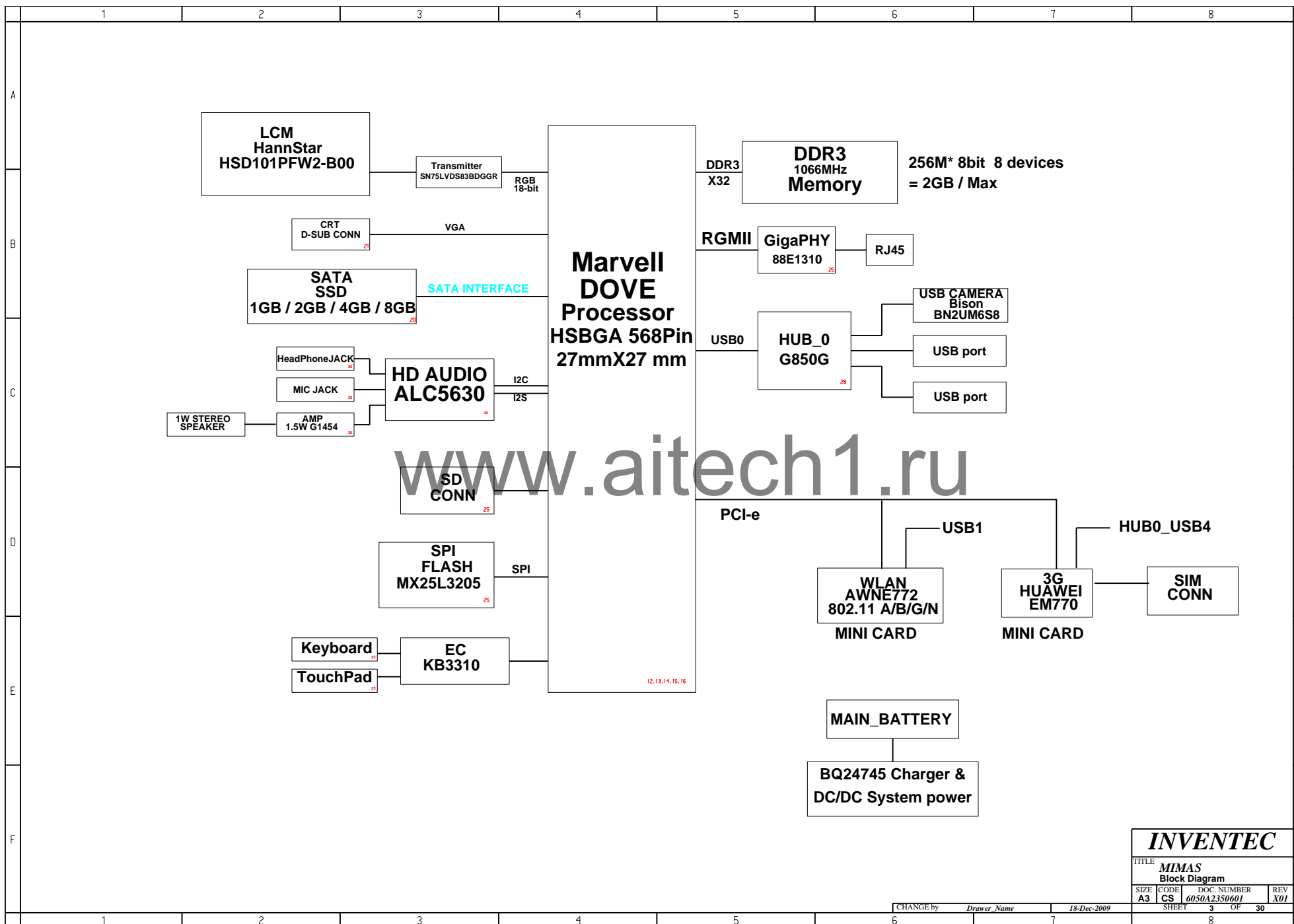
1. COVER PAGE	
2. INDEX	
3. BLOCK DIAGRAM(MOBILE)	
4. BLOCK DIAGRAM(DESKTOP)	
5. POWER SEQUENCE BLOCK	
6. DC JACK & VOLTAGE RESET	
7. 3.3V & 5.0V	
8. 1.8V & 2.5V	
9. 1.5V & VTT	
10. VCORE & VCPU	
11. POWER LOW SWITCH	
12. DOVE1	
13. DOVE2	
14. DOVE3	
15. DDR TERMINATOR	
16. DDR MEMORY	
17. DDR MEMORY	
18. DVI TRANSMITTER	
19. DVI CONN	
20. SD & SATA CONN	
21. CONFUGRATION (STRAPPING)	
22. GIGA PHY & RJ45	
23. USB HUB & 9-PIN HEADER	
24. USB PORT & USB CONN	
25. MINI CARD & KB CONN.	
26. AUDIO JACK	
27. STATUS LED	
28. POWER UP & RESET	
29. MCU & POWER BUTTON	
30. D-sub	

INVENTEC

TITLE
MIMAS

SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X01
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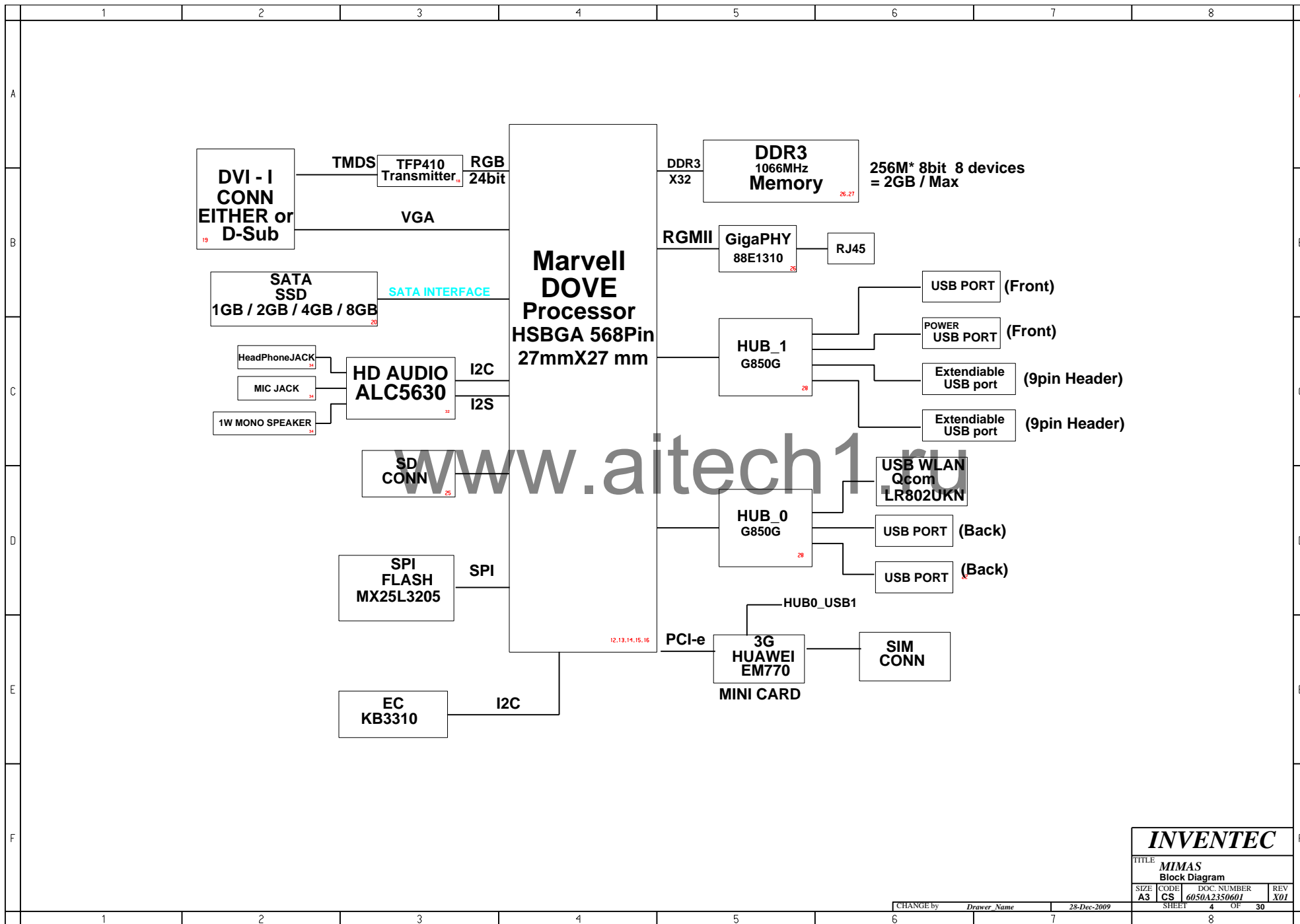
CHANGE by *Drawer Name* 18-Dec-2009

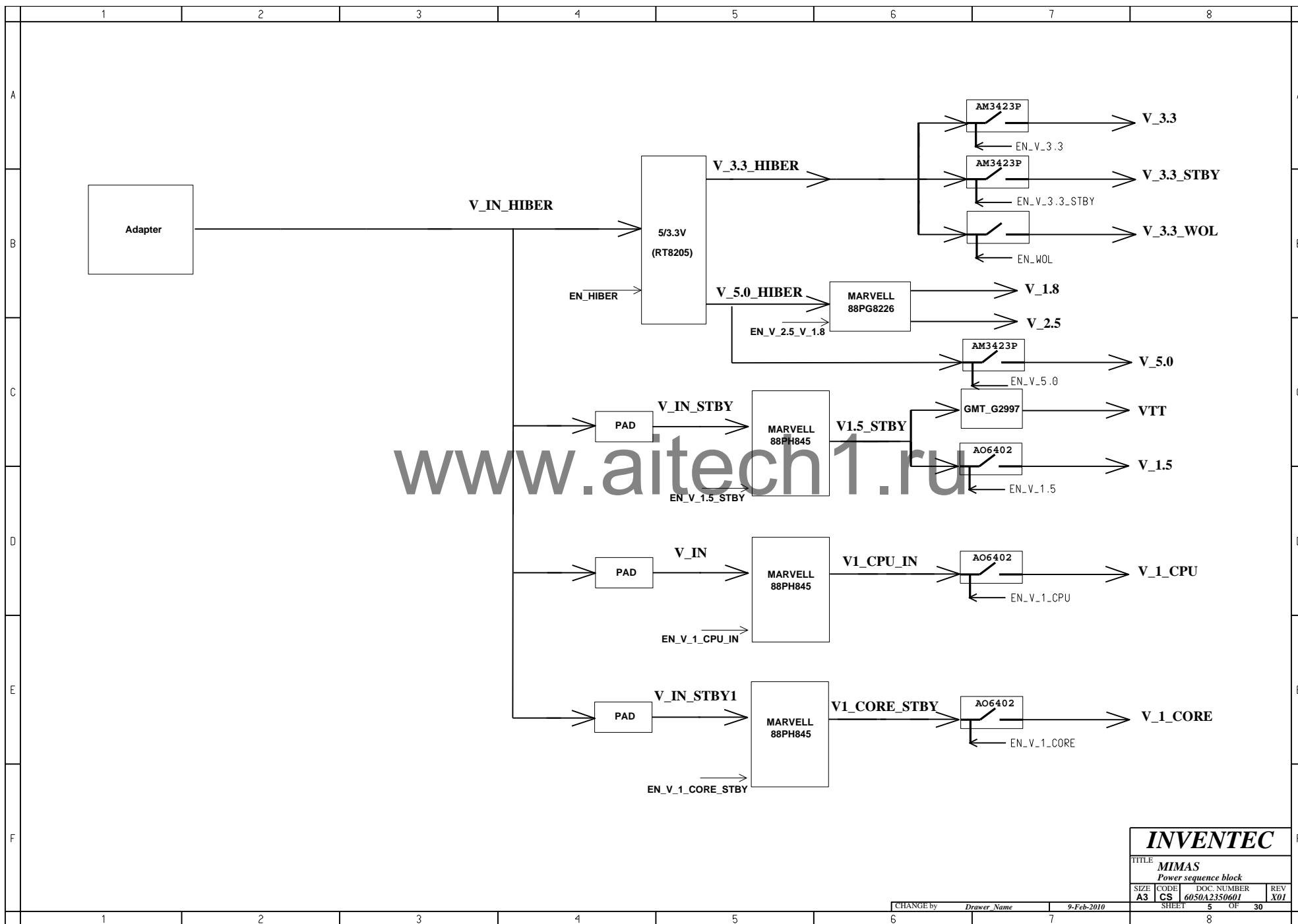


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TITLE MIMAS Block Diagram			
SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X01
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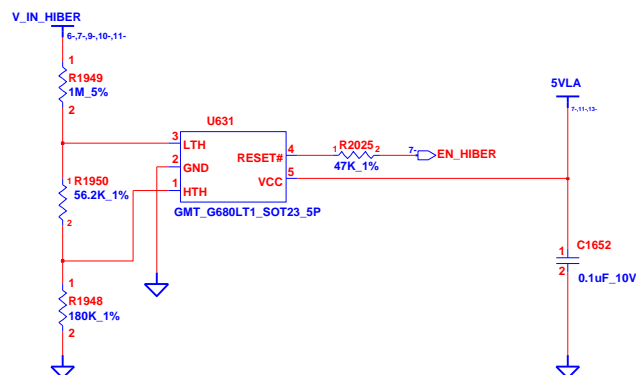
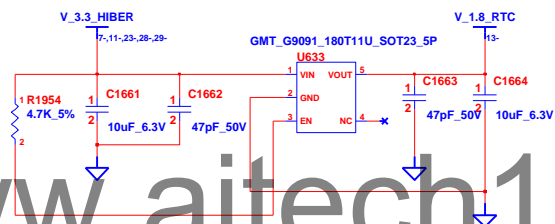
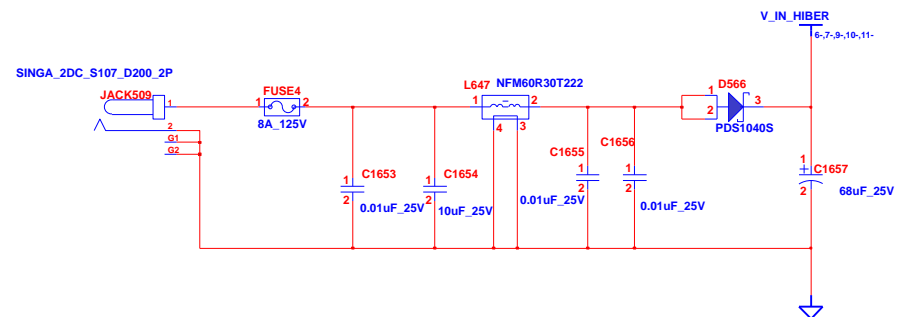


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TITLE			
MIMAS			
Power sequence block			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A2350601	X01

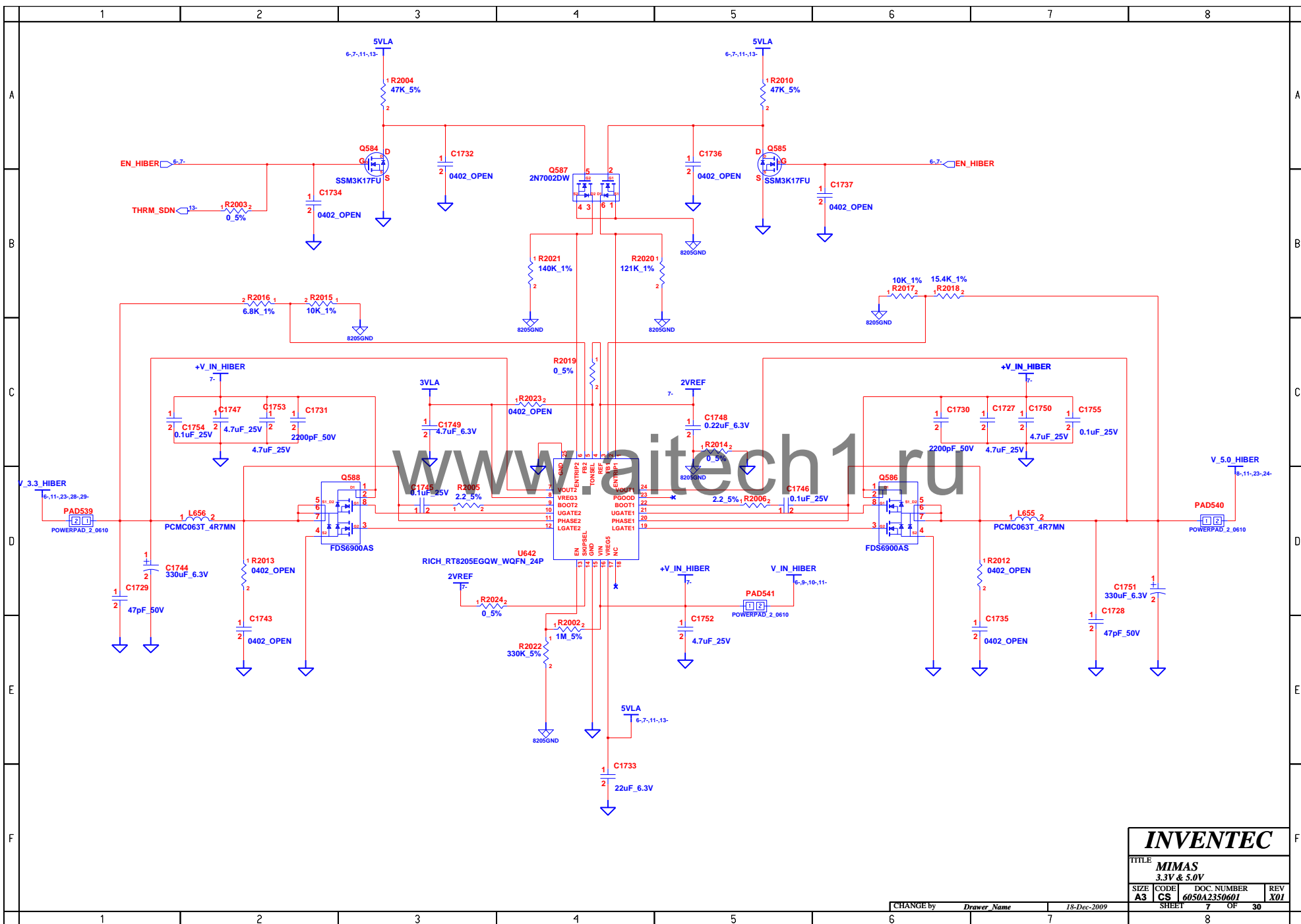
CHANGE by *Drawer Name* 9-Feb-2010

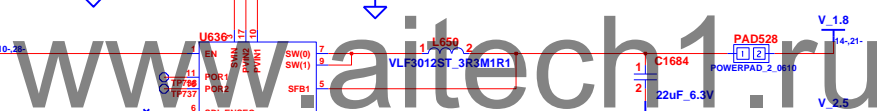
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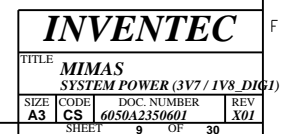
INVENTEC			
TITLE			
MIMAS			
DC JACK & VOLTAGE RESET			
SIZE	CODE	DOC. NUMBER	REV
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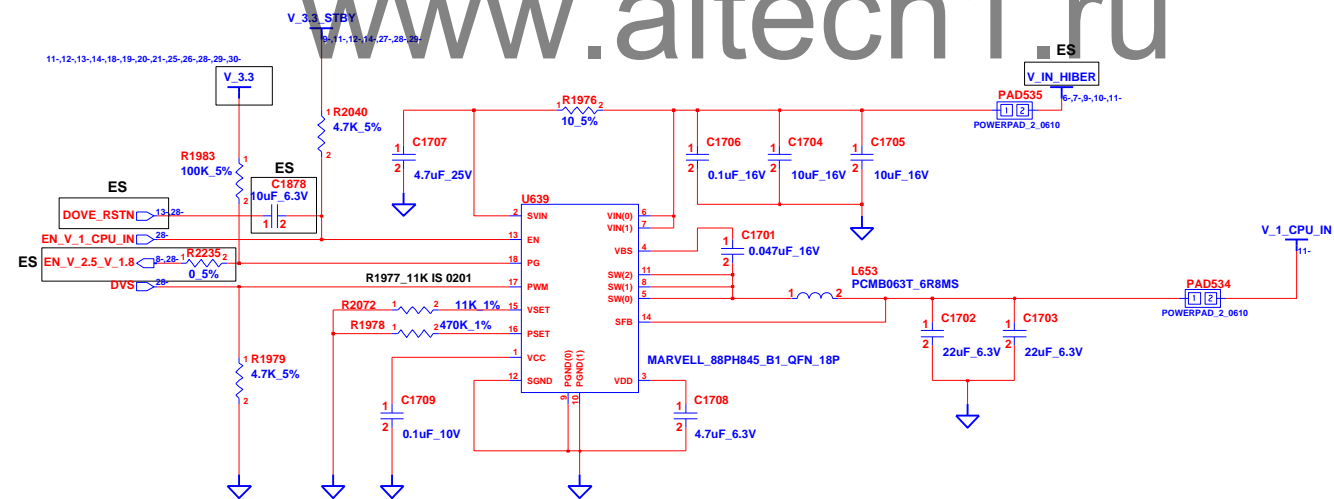
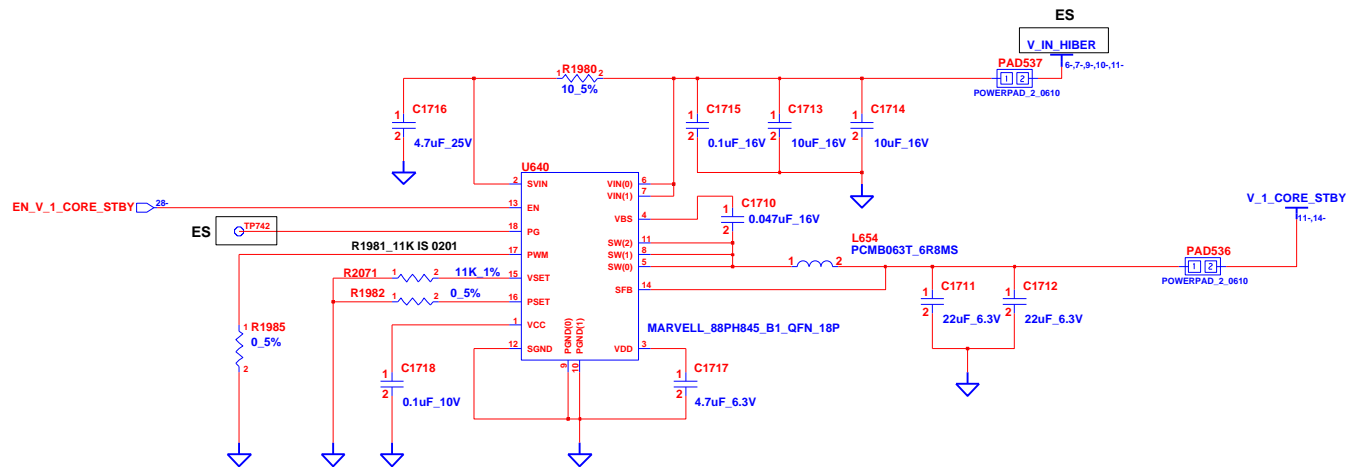
CHANGE by *Drawer Name* 18-Dec-2009





CHANGE by	<i>Drawer_Name</i>	18-Dec-2009
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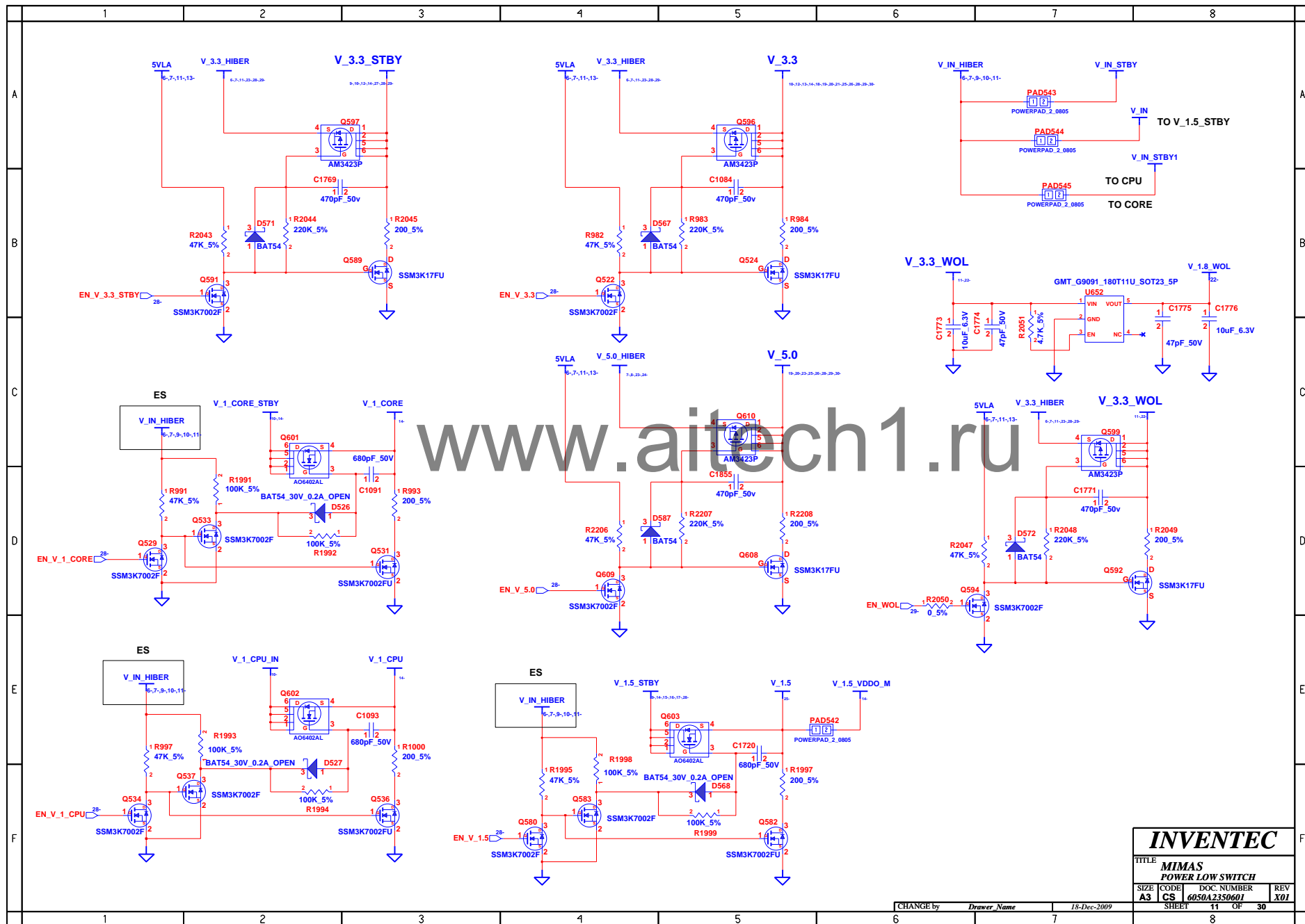
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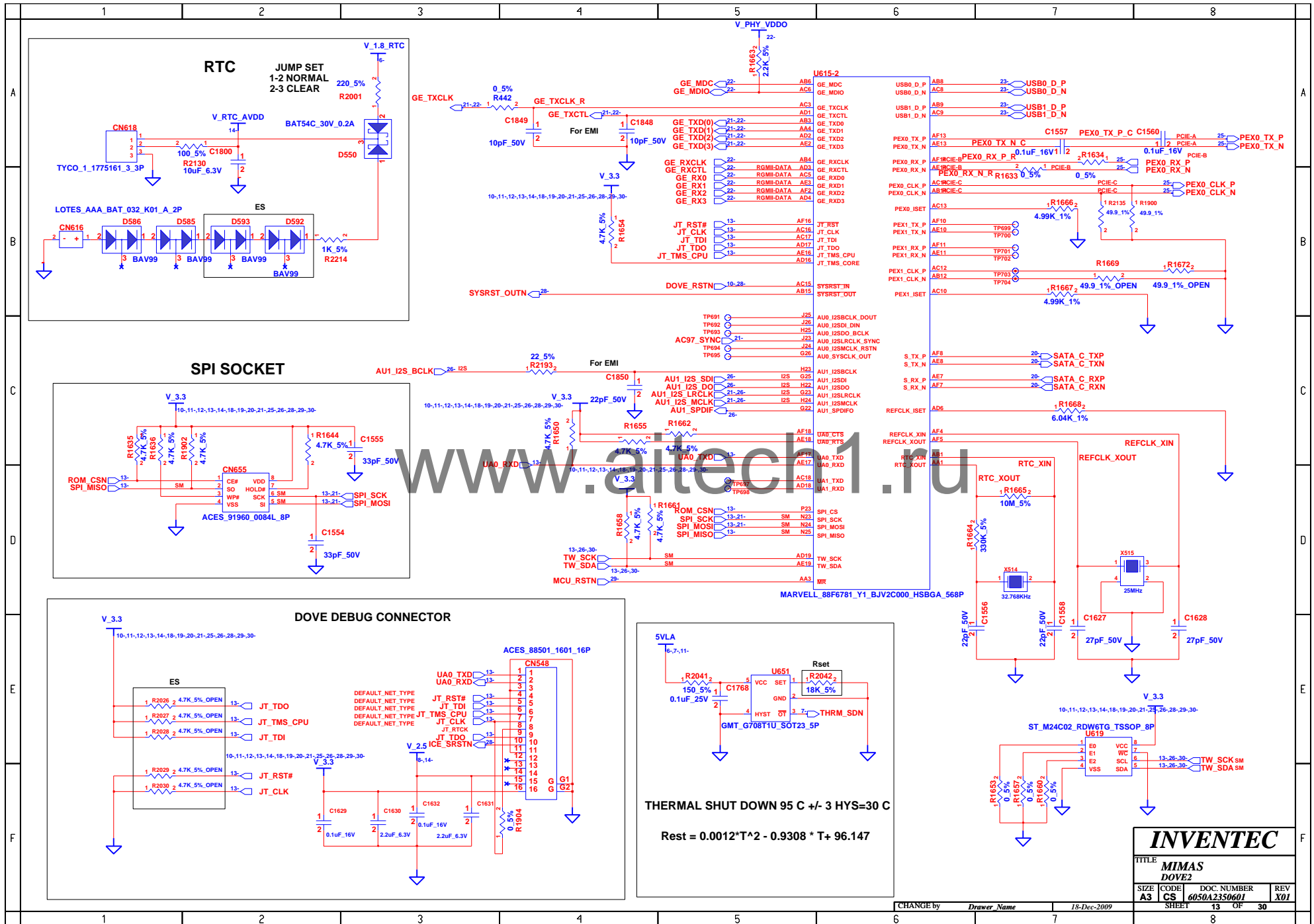
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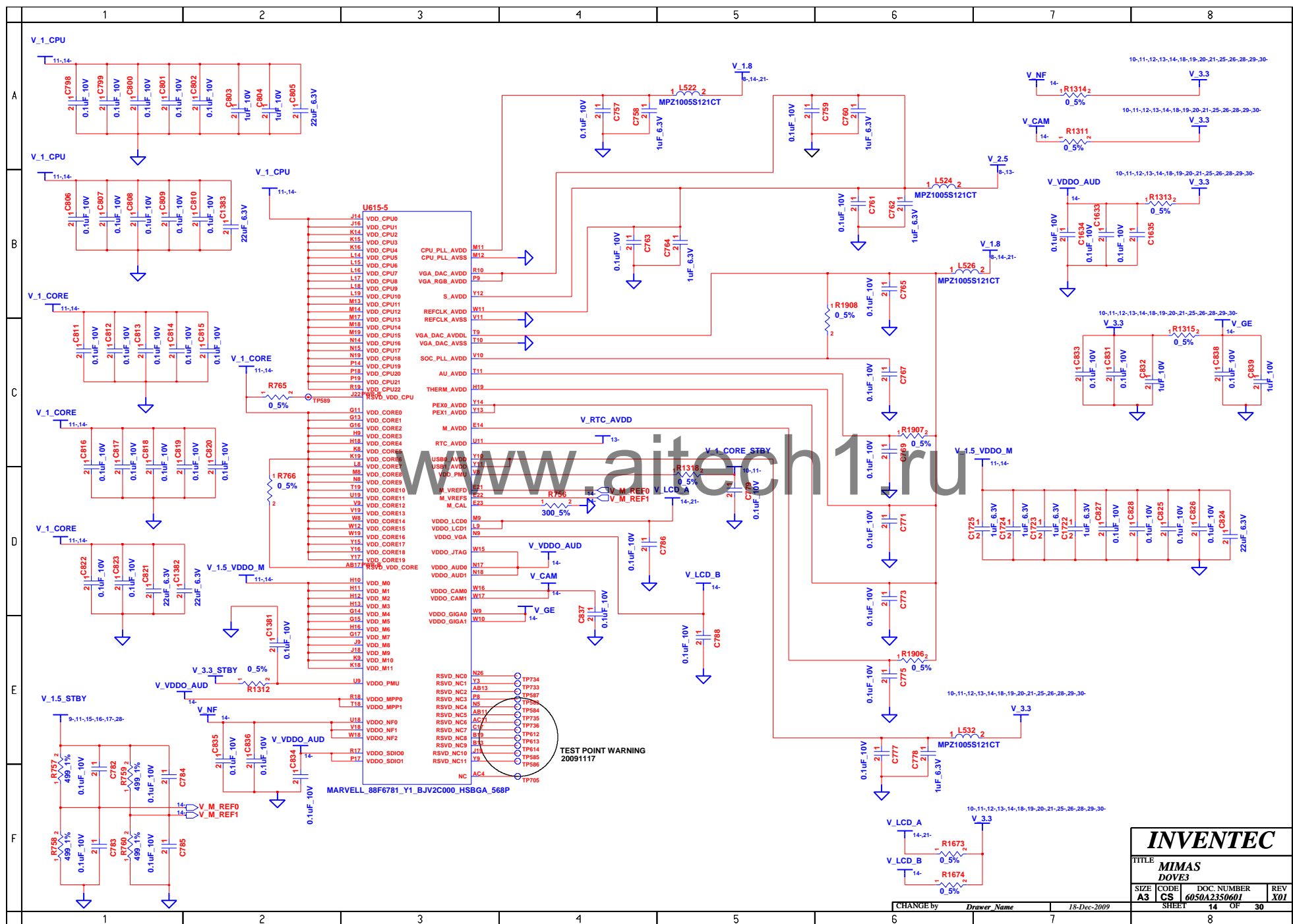
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MIMAS			
VCORE & VCPU			
SIZE	CODE	DOC. NUMBER	REV
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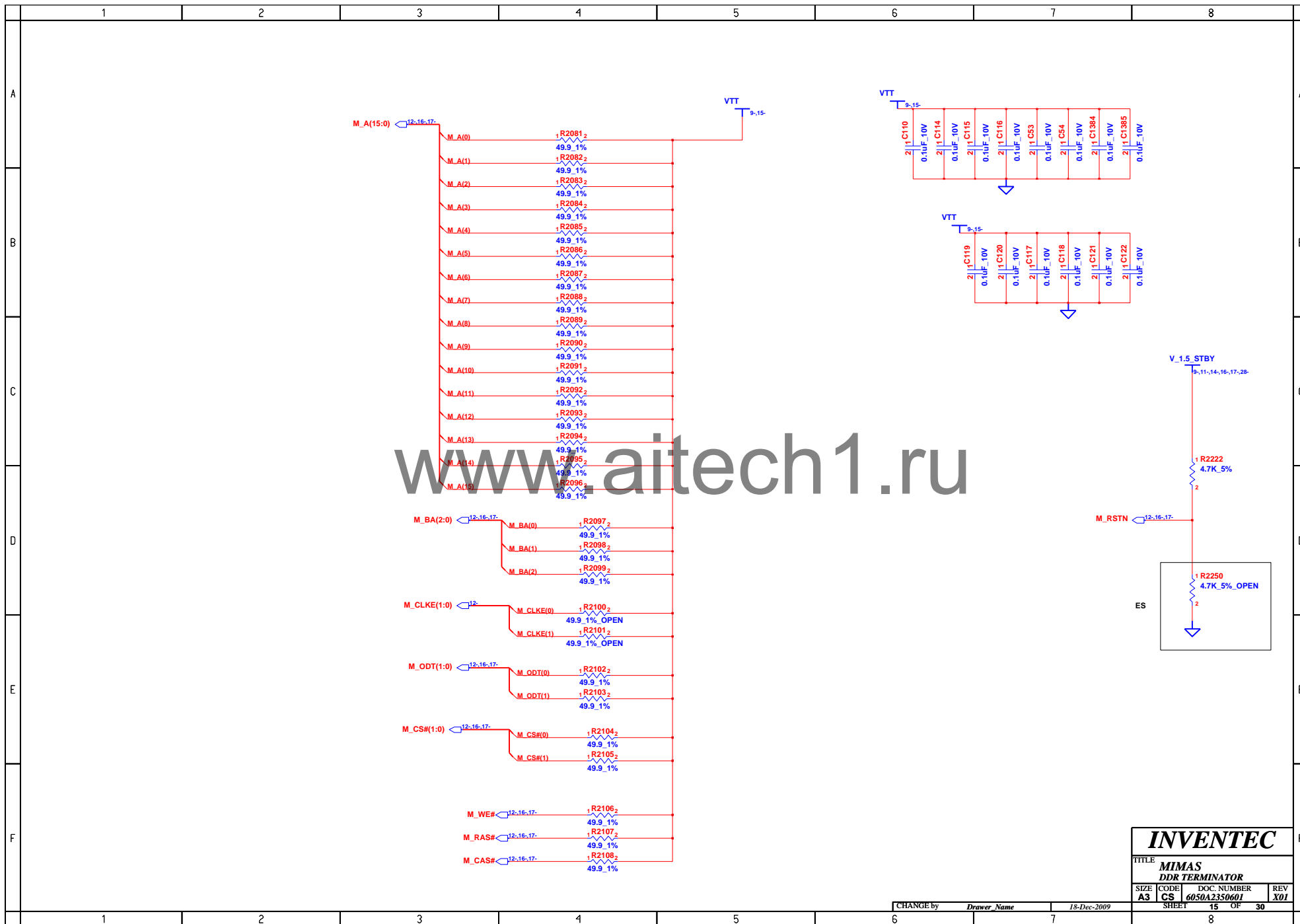
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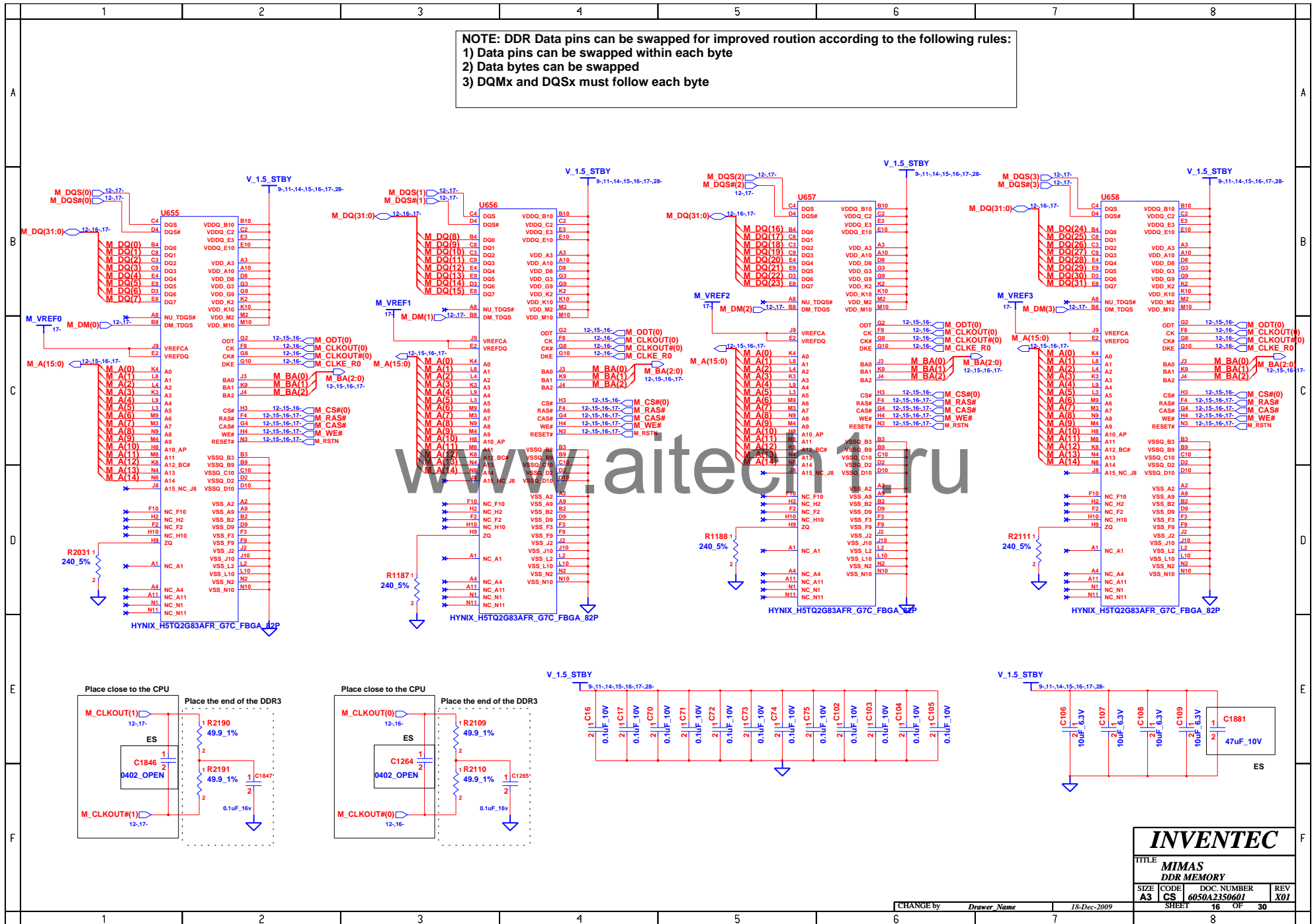


INVENTEC

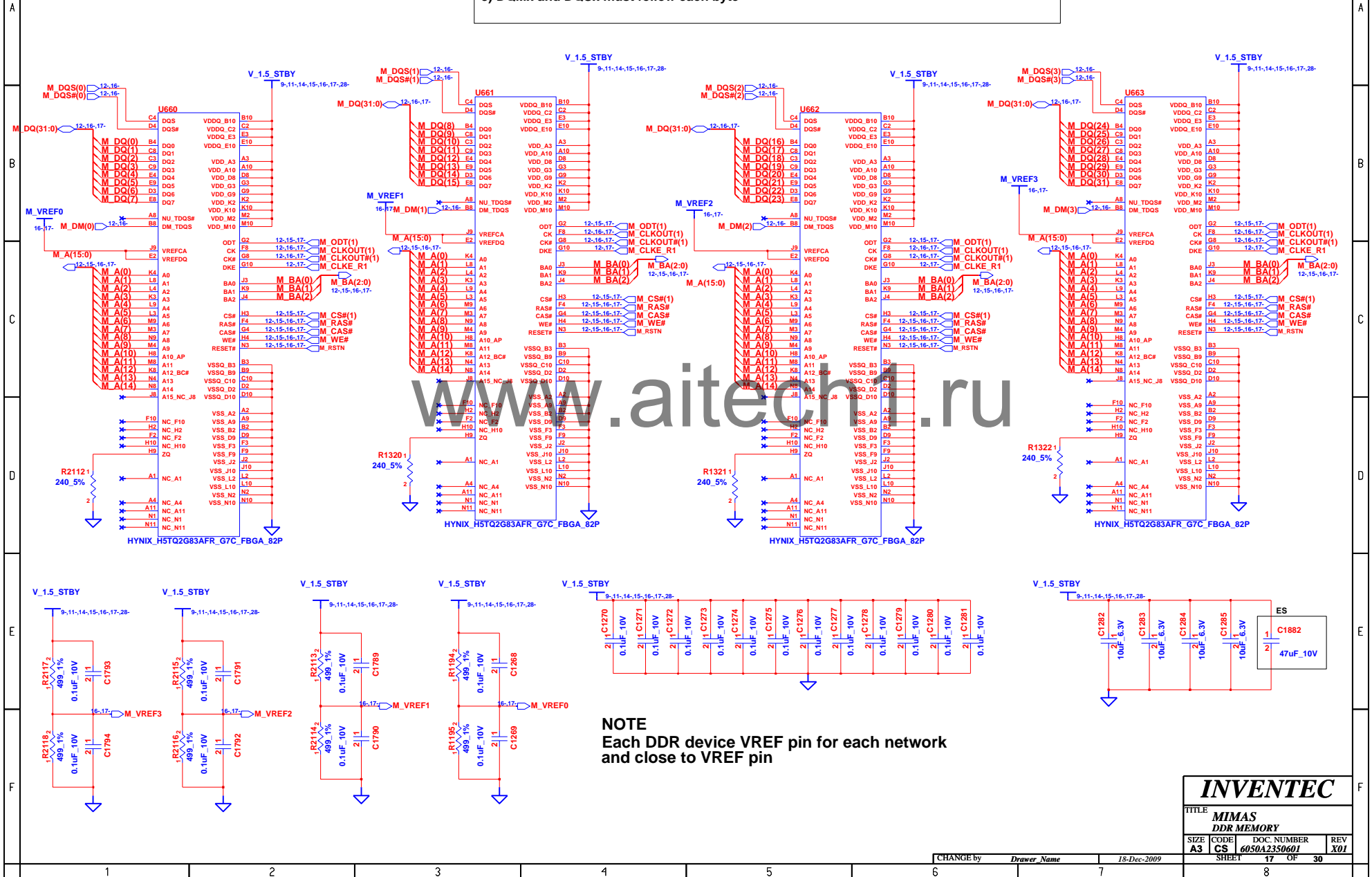
TITLE			
MIMAS			
DDR TERMINATOR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A2350601	X01
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NOTE: DDR Data pins can be swapped for improved routin according to the following rules:
 1) Data pins can be swapped within each byte
 2) Data bytes can be swapped
 3) DQMx and DQSx must follow each byte



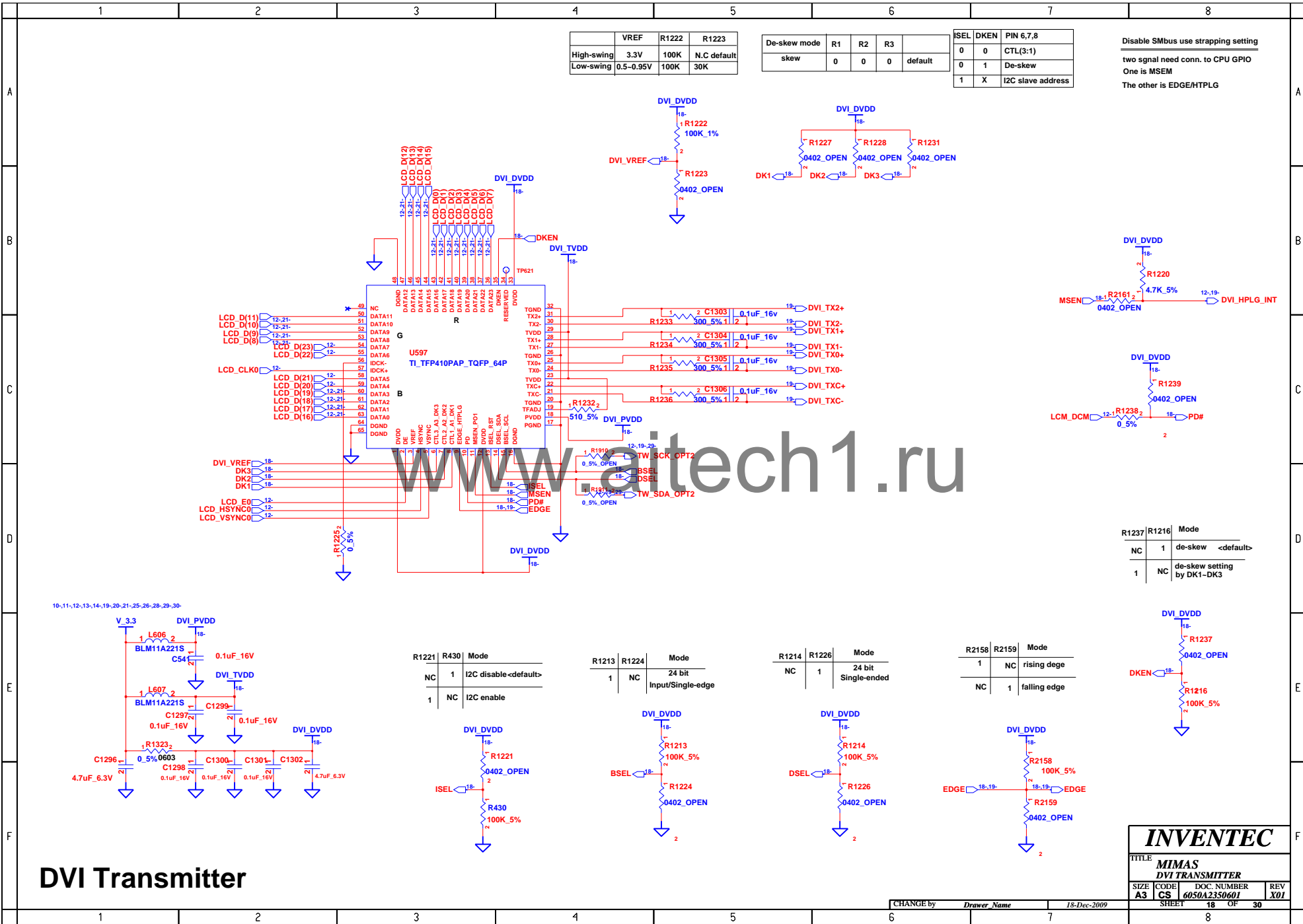
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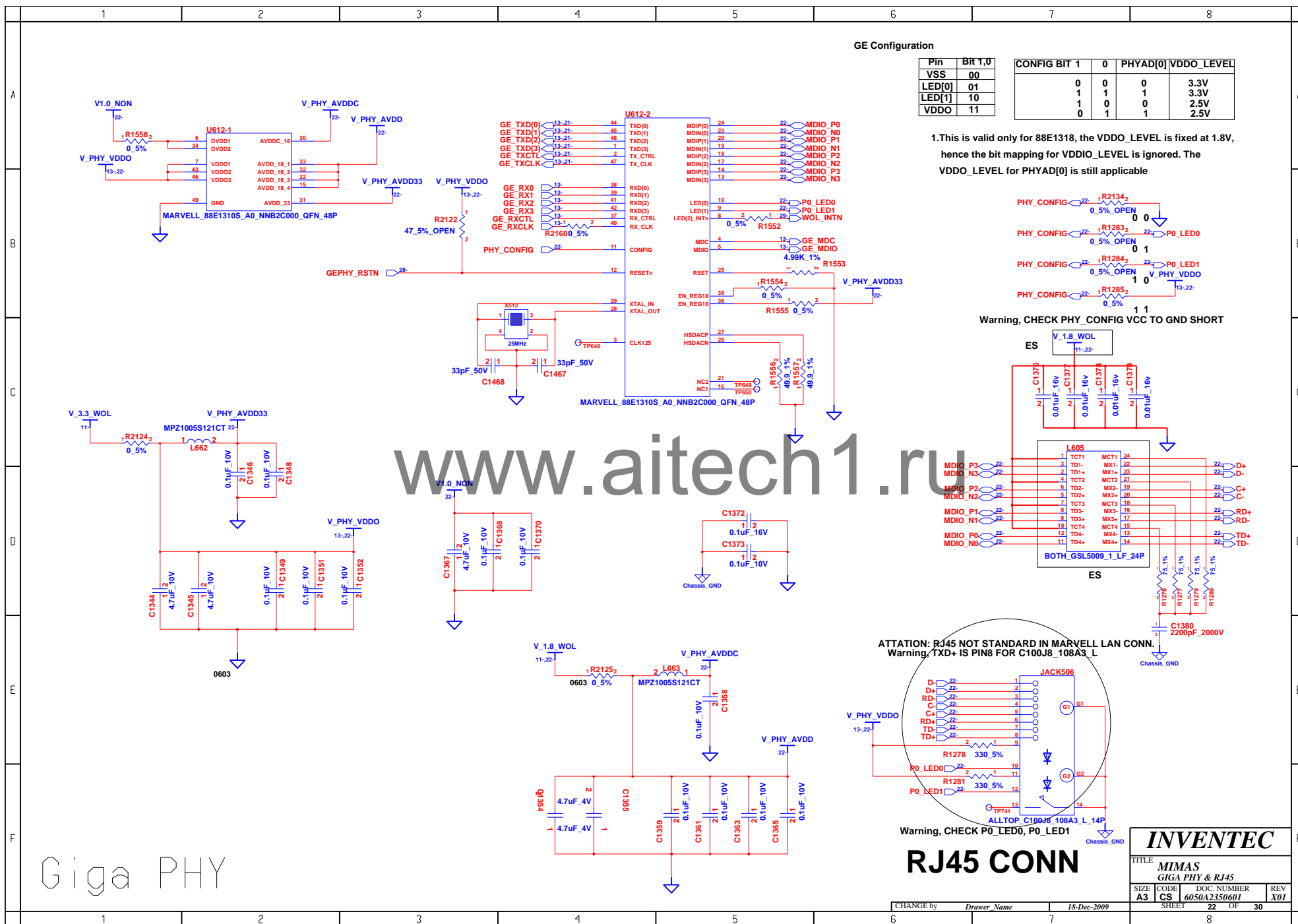
INVENTEC

TITLE
MIMAS
DDR MEMORY
SIZE CODE DOC. NUMBER REV
A3 CS 6050A2350601
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CHANGE by *Drawn Name* 18-Dec-2009



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TITLE MIMAS DVI CONN			
SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X01
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305mA

Layout Note : Refer to AN15.17 Rev1.1

MINI_CARD

WLAN

Back side

Back side

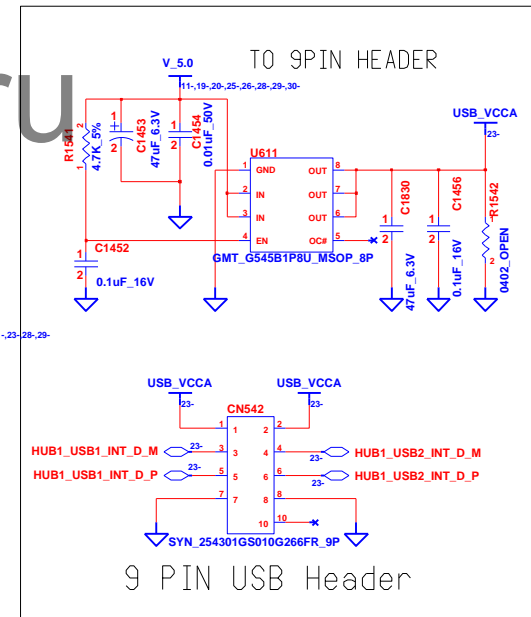
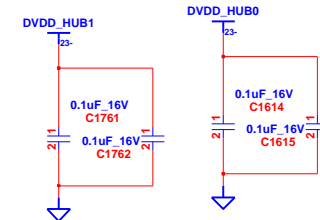
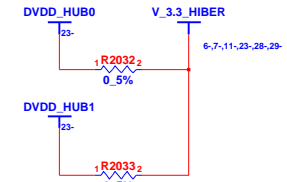
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Int 9 pin

Int 9 pin

Front side

Front side

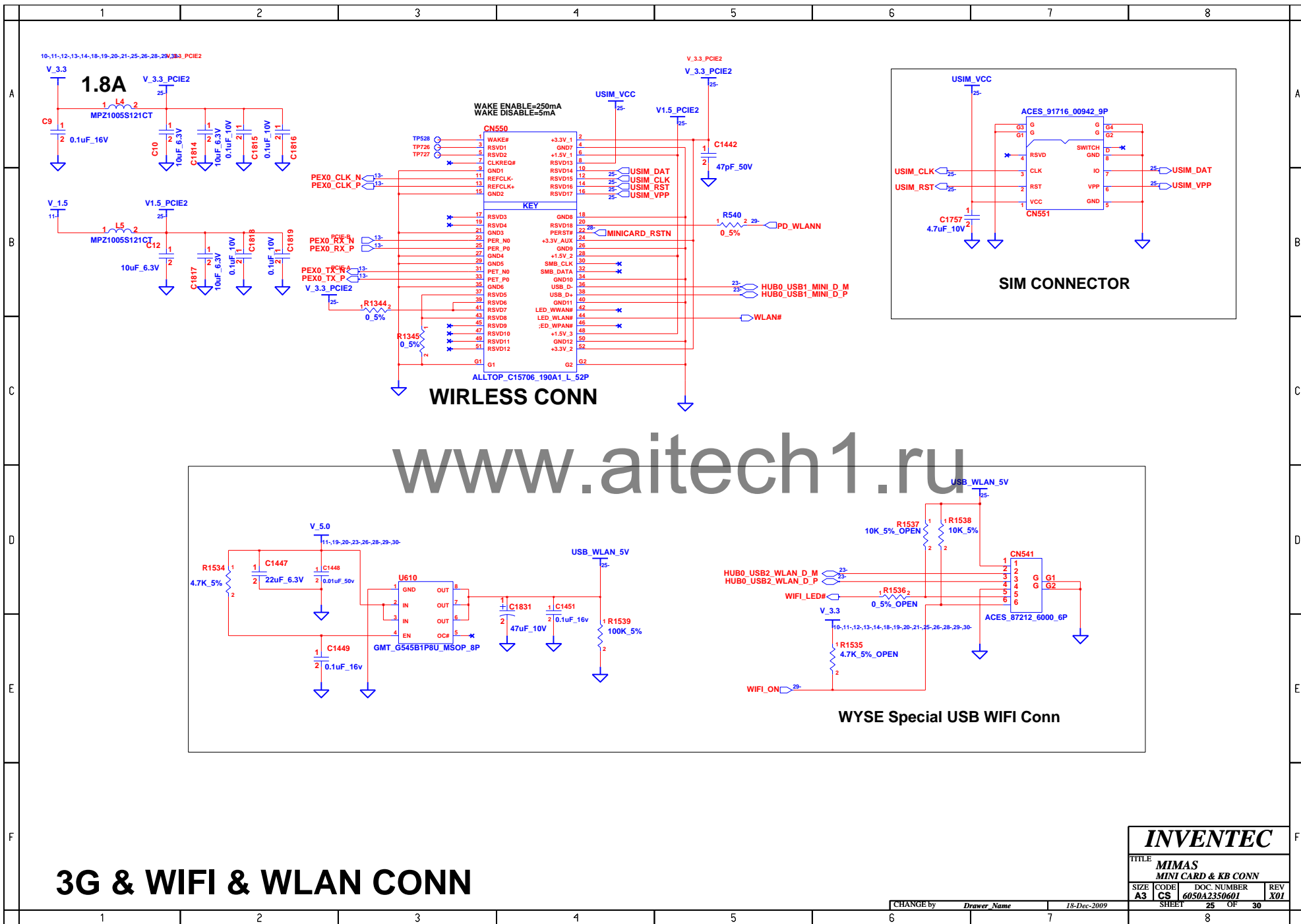


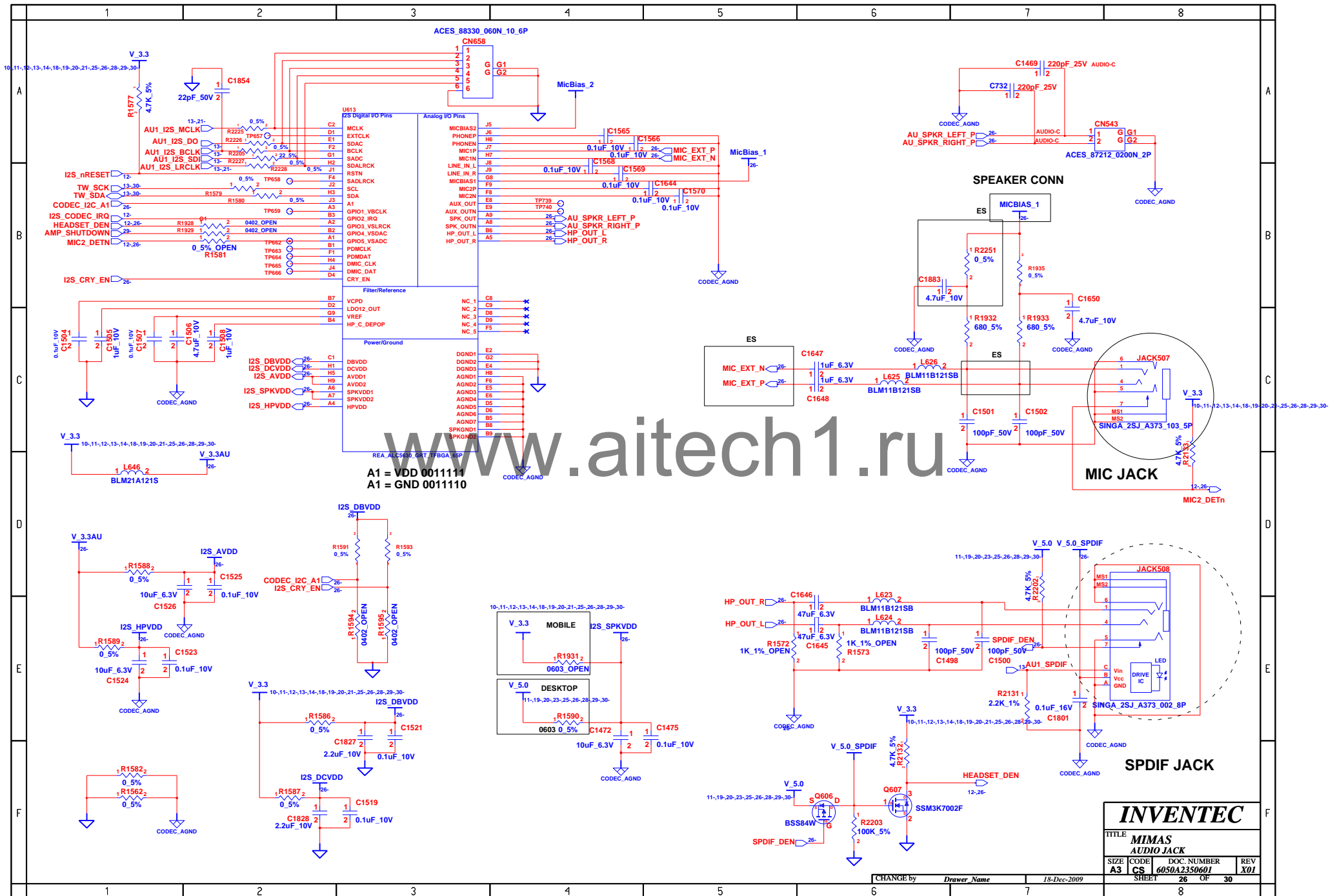
INVENTEC

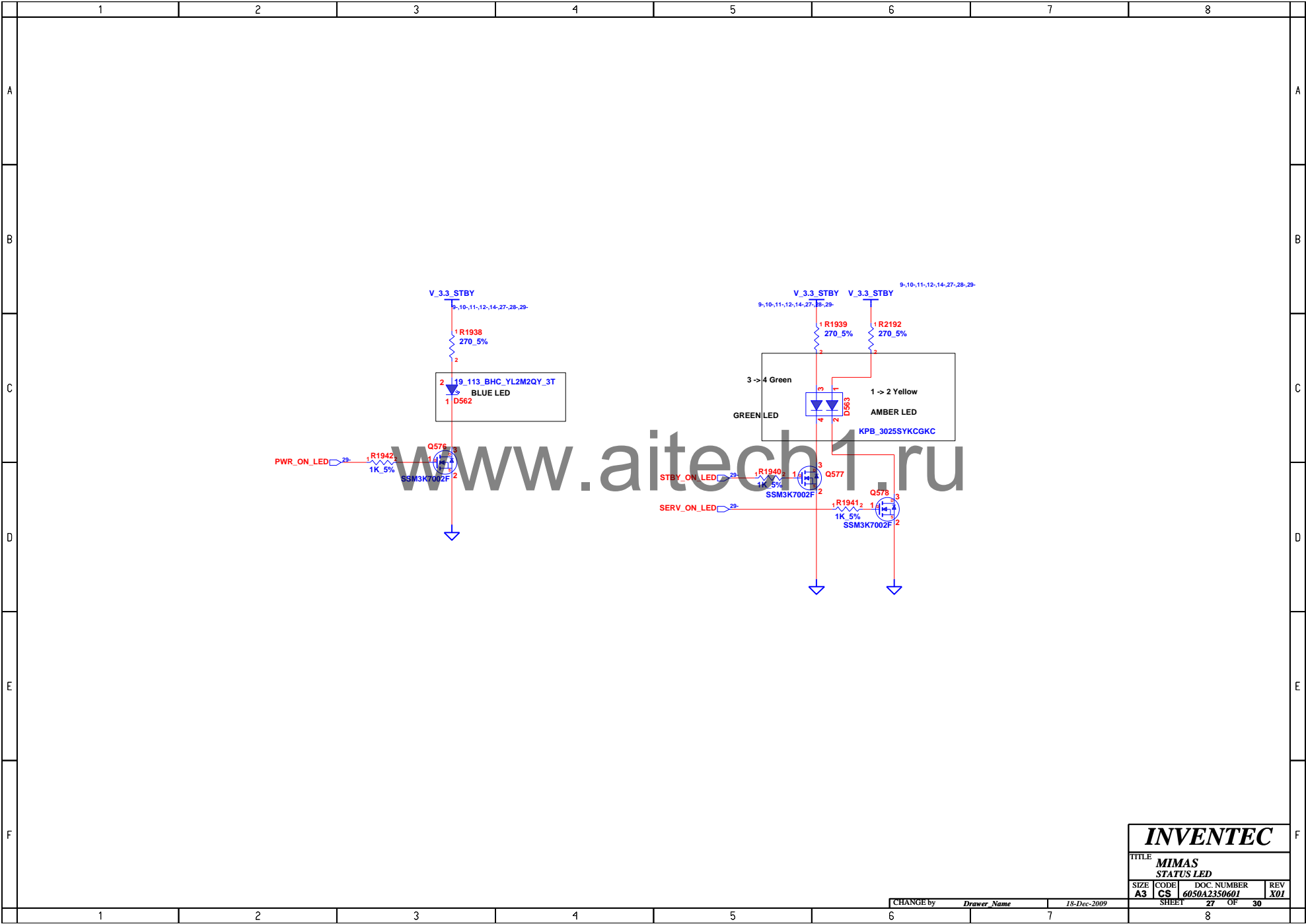
TITLE			
MIMAS			
USB HUB & 9-PIN HEADER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	6050A2350601	X01

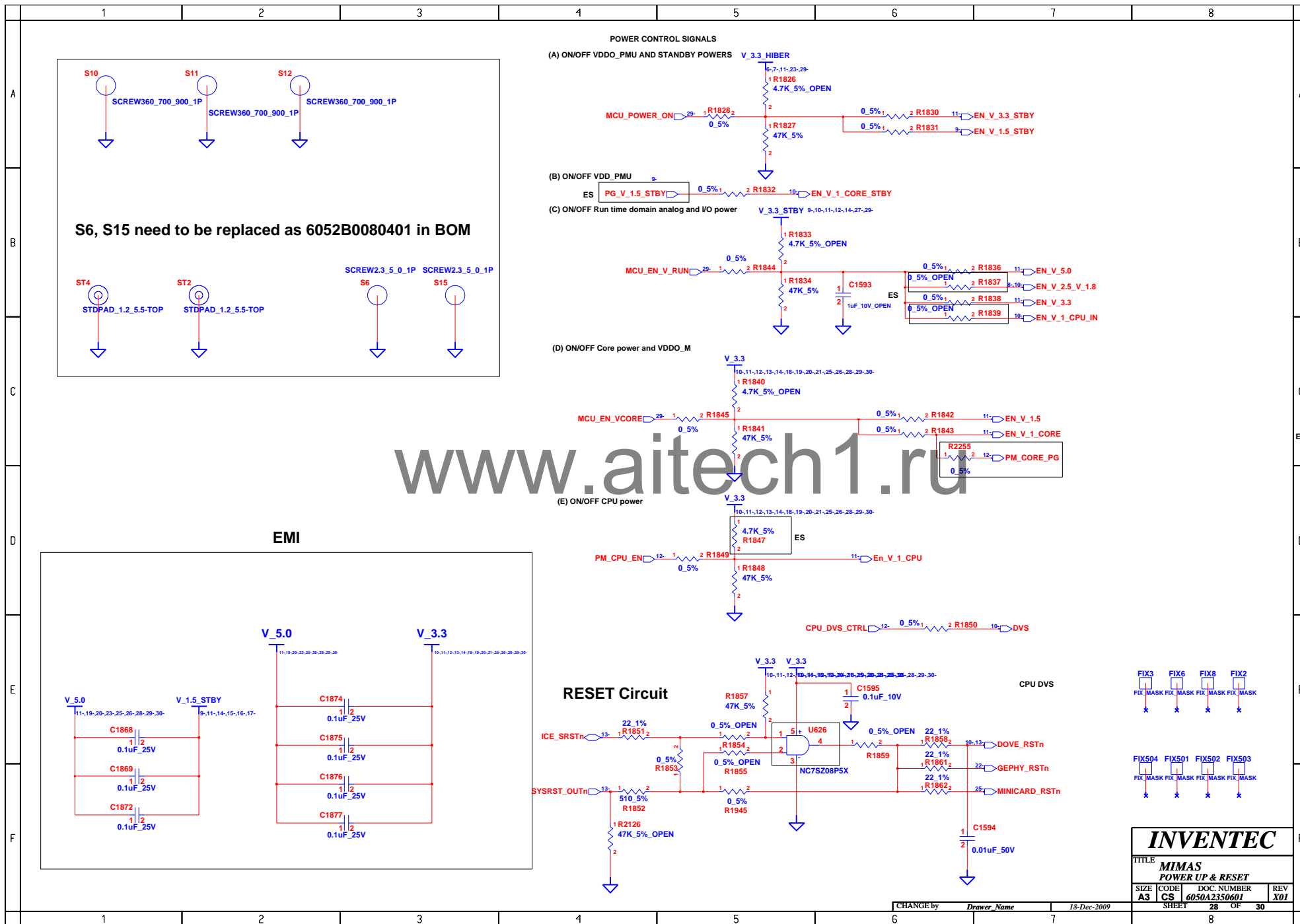
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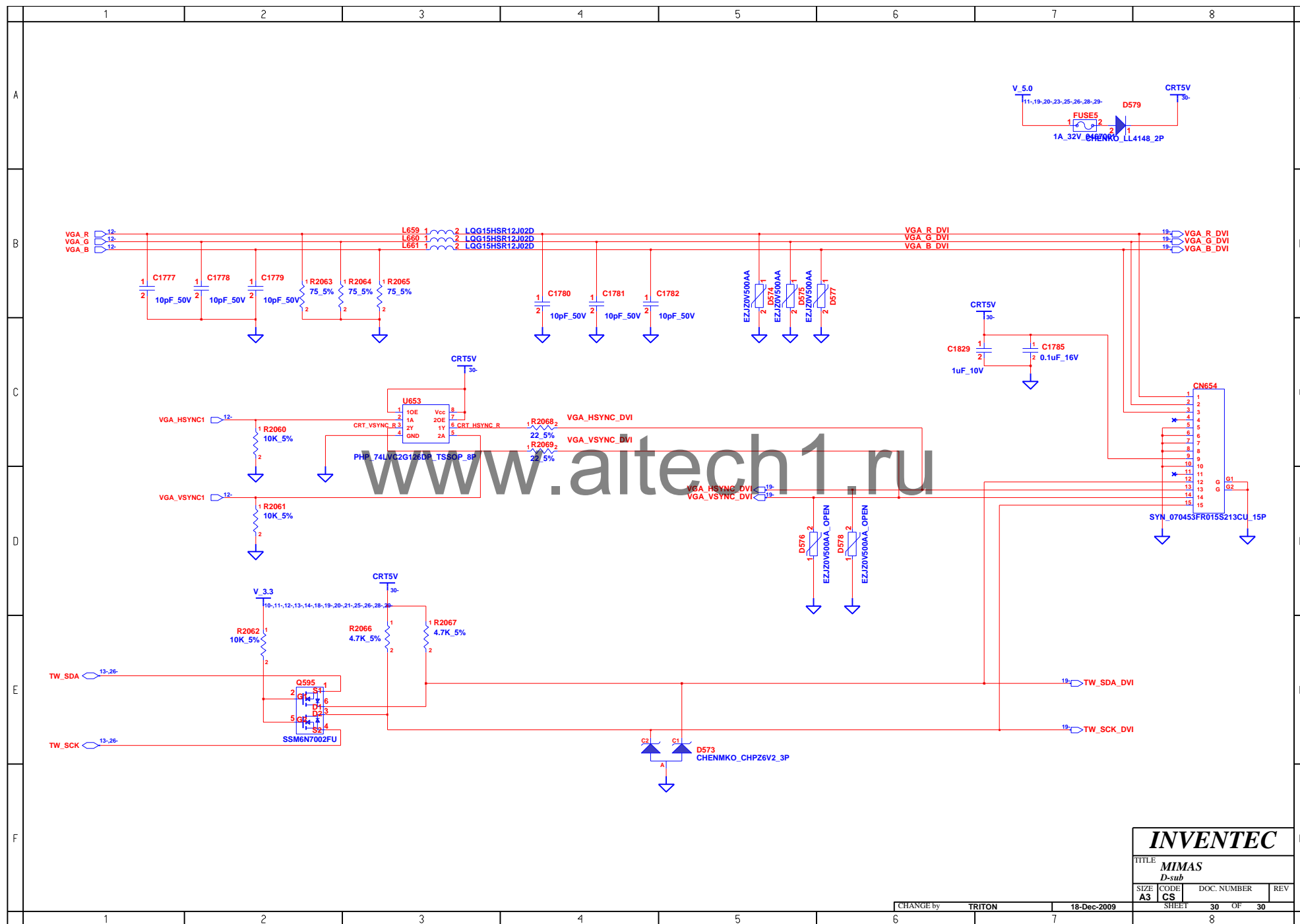




S6, S15 need to be replaced as 6052B0080401 in BOM

EMI

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TITLE			
MIMAS			
D-sub			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	SHEET	30 OF 30
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CHANGE by TRITON 18-Dec-2009